In the Claims:

Kindly rewrite the claims to read as follows:

1. (Currently amended) Device for detecting electromagnetic radiations~~, and in particular infrared radiations,~~ implementing a detection circuit associated with a reading circuit, the detection circuit comprising an array of detection pixels, each of said pixels comprising a thermal detector of biased bolometric type, and delivering an electric current signal representative of detected radiation, said current signal undergoing a double baselining, respectively:

- a global baselining carried out by means of a thermally isolated bolometer, ensuring extraction from said electric current signal, of a first current of constant value inherent to biasing of the thermal detector, and
- an adaptive baselining specific to each of the pixels, carried out by means of a programmable current generator, specific to each of the pixels, generating a current for subtraction from said signal, as a function of dispersion inherent to the pixel considered relative to a reference signal and stored in an associated memory,

wherein said associated memory ~~is integrated~~ comprises an internal memory physically implanted at a level of each of said pixels.

2. (Previously presented) Device for detecting electromagnetic radiations according to Claim 1, wherein a phase of reading data of each of memories associated with said pixels occurs between an end of integration of a row n and start of integration of a row n+1 of the array of said pixels.

3. (New) Device for detecting electromagnetic radiations according to Claim 1, wherein said electromagnetic radiations comprise infrared radiations.

4. (New) Device for detecting electromagnetic radiations according to Claim 1, wherein said internal memory is integrated into a surface of an associated pixel.

5. (New) Device for detecting electromagnetic radiations according to Claim 4, wherein said internal memory comprises a static RAM.

6. (New) Device for detecting electromagnetic radiations according to Claim 1, wherein said associated memory comprises 3 internal memories physically implanted with each pixel for storing 3 bits for each pixel.